

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.: JP920000113US1

In re Application of: S. Yamada

Examiner: Tammara R. Peyton

Serial No.:

09/838,994

6,842,801 B2

Patent No.:

January 11, 2005

Art Unit:

2182

Issued:

For:

System and Method of

§ §

Implementing a Buffer Memory and Hard Disk Drive Write

Controller

REQUEST FOR CERTIFICATE OF CORRECTION

§

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Applicant respectfully submits the enclosed proposed Certificate of Correction, which corrects clerical errors made by the U.S. Patent and Trademark Office in the Letters Patent No. US 6,842,801 B2, issued January 11, 2005.

Certificate
JAN 1 8 2006

of Correction

Respectfully submitted,

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CERTIFICATE OF MAILING 37 C.F.R. § 1.8(a)

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

		Danie 4 6 4
PATENT NO. :	6,842,801 B2	Page1_ of1_
APPLICATION NO.:	09/838,994	
ISSUE DATE :	01/11/2005	
INVENTOR(S) :	Shuji Yamada	
It is certified is hereby correct	d that an error appears or errors appear in the above-identified patent and the deas shown below:	nat said Letters Patent
At column 14, lir	ne 58, delete "fall" and insertfull	
At column 16, lir	ne 37, delete "riot" and insertnot	

MAILING ADDRESS OF SENDER (Please do not use customer number below):

DILLON & YUDELL LLP 8911 N. Capital of Texas Hwy., Suite 2110 Austin, TX 78759

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